Rec**k**ipt date: 01/30/2006



## IN THE UNITED STATES DESIGNATED OFFICE (DO/EO/US) 5 0 JAN 2006

**ATTY DKT. # ONS00396** 

Date

1/24/2006

IN RE APPLICATION OF

Francois LHermite

APPLN. NO.

PCT/US2003/030581

**FILED** 

9/23/2003

FOR

NEGATIVE CURRENT COMPENSATION METHOD AND

**CIRCUIT** 

## **DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.56**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SIR:

It is respectfully requested that the prior art listed on FORM PTO-1449 be considered in the examination of the subject application and made of record therein. A copy of the listed prior art is enclosed herewith.

No representation is made or intended that the listed prior art enclosed herewith is material to patentability of the subject patent application.

No representation is made or intended that a search has been made or that no better prior art than that listed is available.

Respectfully submitted, François L'Hermite

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Sheet  $\underline{1}$  of  $\underline{1}$ 

(Form PTO-1449)

FORM PTC 1449	)- U.S.	DEPARTMENT OF COMMERCE	I CN2003AD	0.		SERIAL NO. PCT/US2003/0 30581	
	PATENT AND TRADEMARK OFFICE		<b>I</b>		1	<b>3756</b> 65	
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(Use several sheets if necessary)			FILING DATE 23 September 200	)3		GROUP	
		U	.S. PATENT DOCUM	ENTS			
EXAMINE R INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS		FILING DATE IF APPROPRIATE	
/A.B./	5784231	7/21/1998	Majid et al.	НО2Н	7/00		
				-			
		FOR	EIGN PATENT DOCU	JMENTS	<u> </u>		
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATIO N	
						YES NO	
	OTHER	DOCUMENTS (In	Luding Author, Title	, Date Pertinent	Pages, Etc.)		
/A.B./	Data Sheet, In 2002, Philips	Data Sheet, Integrated Circuits "TEA1533P; TEA1533AP GreenChip ™II SMPS contro 2002, Philips Semiconductors, 23 pps.					
/A.B./	Data Sheet, In	Data Sheet, Integrated Circuits "TEA1552 GreenChip TMII SMPS control IC", August 27 2002, Philips Semiconductors, 23 pps.					
EXAMINER /Ad		/Adolf	Berhane/			DATE 08/27	